

### **Key Features & Benefits**

- Standard output voltages of 1.5 to 54 VDC
- Efficiencies up to 87% typical
- Extra-Low 1U profile: 1.6 inch
- High power density design of 14 Watts/cubic inch
- 1 to 4 isolated output slots, fully user configurable
- Power Factor Correction (PFC) IEC 61000-3-2 compliant
- 700W @ 85 VAC or 900W @ 150VAC of total output power
- Zero-load operation
- Single-wire current sharing
- Universal input AC range
- Individual control signals on each module
- Auxiliary power 5V (1A)

### **Applications**

#### LPM409 (Industrial)

- Industrial equipment
- Telecommunications
- Test and measurement
- Automation
- Peripherals
- Audio/broadcast
- Linear and rotary motion

#### LMM409 (Medical)

- Imaging Equipment
- Anesthesiology
- Surgical Devices
- Diagnostic Equipment
- Patient Beds
- Home Healthcare
- Ultrasound

# LPM/LMM409 AC-DC Modular Power Supply

5V, 10V, 12V, 20V, 24V, 36V & 48V Single-Output Modules; 10V, 20V Dual-Output Modules

The LPM/LMM409 Series is a modular 900-watt AC-DC power supply that provides a market-leading power density of 14 watts per cubic inch and has an extra low 1U profile. The LMM is the medically approved version. From here on only the LPM version will be discussed, differences for the LMM version will be noted. The LPM409 offers the flexibility of a modular architecture and the combination of high efficiency and high power density.

Designed for use where a unique set of voltage and current requirements is needed, the supply's four slots can be configured with PCB-based output modules to deliver up to eight outputs. The LPM output modules operate in any chassis position and can provide up to 900 watts total power from a 150 VAC input and 700 watts from a 85 VAC input. Forced-air cooling with airflow direction from input to output is provided by an internal fan. For LPM409 supplies using less than the four-slot capability, blanking plates are installed for safety purposes and to optimize airflow within the chassis. The supplies are pre-set with default output module settings or with the customer's desired output settings prior to delivery. The LPM409 chassis can be populated with the output modules listed in Module Selection table.

> North America +1-866.513.2839

**Asia-Pacific** +86.755.29885888

**Europe, Middle East** +353 61 225 977

Bel Power Solutions 152 North 3rd Street, Suite 805 San Jose, CA 95112 USA



#### 1. Module Selection

| MODULE | P/N             | NO. OF SLOTS<br>REQUIRED | FACTORY-SET SINGLE- OUTPUT (VDC) | MODULE<br>ADJUSTABLE<br>RANGE (VDC) | MAX. OUTPUT<br>CURRENT<br>(AMPS) | MAX. OUTPUT POWER <sup>2</sup> (WATTS) |
|--------|-----------------|--------------------------|----------------------------------|-------------------------------------|----------------------------------|--|
| Е      | LPM126-OUTA1-05 | 1                        | 5                                | 2.0 to 5.3                          | 53                               | 265                                    |
| F      | LPM126-OUTA1-12 | 1                        | 12                               | 5.2 to 15                           | 22                               | 265                                    |
| G      | LPM126-OUTA1-24 | 1                        | 24                               | 14 to 30                            | 11                               | 265                                    |
| Н      | LPM126-OUTA1-36 | 1                        | 36                               | 29 to 44                            | 7.4                              | 265                                    |
| J      | LPM126-OUTA1-48 | 1                        | 48                               | 43 to 54                            | 5,5                              | 265                                    |
| K      | LPM109-OUTA1-10 | 1                        | 15                               | 1.5 to 15                           | 6                                | 90                                     |
| L      | LPM109-OUTA1-20 | 1                        | 30                               | 3 to 32                             | 3                                | 90                                     |
| M      | LPM118-OUTA2-10 | 1                        | 15                               | 2x 1.5 to 15                        | 2x 6                             | 2x 90                                  |
| N      | LPM118-OUTA2-20 | 1                        | 30                               | 2x 3 to 32                          | 2x 3                             | 2x 90                                  |
| 0      | LPM100-BLAN     | 1                        |                                  | Blank Panel                         | (Slot Cover)                     |  |

To determine your desired power supply's part number, please refer to *Figure 2* for a detailed part number description. Use the **LPM Configurator** located in the Modular Section on our website, or use our contacts page to locate a distributor for further assistance.

### 1.1. Output Voltage Adjustment

Each LPM409 module's output voltage is adjustable by means of a trimmer located on the module. For the modules E<->J the trimmer is accessible through the adjustment hole located on the bottom of the power supply (*Figure 10*). For the modules K<->N the trimmer is accessible from rear side of the power supply (*Figure 5*).

#### 1.2. Parallel Connections

Depending on certain situations where current requirements exceed one module's capability, the configurator will process a solution using parallel connections. Paralleling busbars are available to make connections requiring higher current needs. For correct current sharing functionality it is necessary to interconnect Current Share pins of the modules that will operate in parallel using signal wires. Only E-J modules can be connected in parallel. For more details read section 10.3, see *Figure 15*.

#### 1.3. Serial Connections

To increase output voltage it is possible to connect modules in series. Serial busbars are available (see section 10.4) to make connections requiring higher voltage needs. The output of LPM409 is rated as SELV circuit, so the output voltages are guaranteed to be less than 60V. The series combination of modules can exceed this SELV voltage, so in this case the users must take adequate precautions. The maximal total voltage **must not exceed 200V**. It is recommended to only connect the same modules with the same current rating in series.

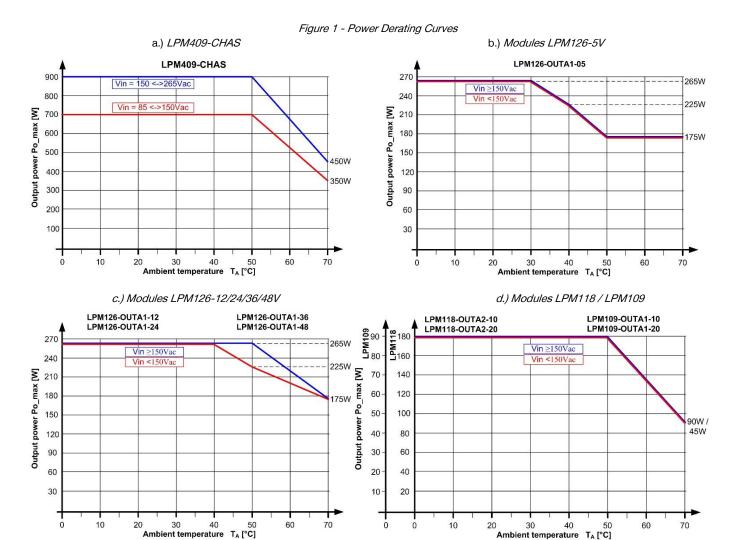
The voltage sense pins VS+, VS- available on LPM126 modules must be connected according to Figure 17.

#### 1.4. Power Derating

When specifying an LPM409 power supply in an application it is necessary to ensure that LPM409-CHAS and modules (LPM126, LPM118 and LPM109) are operating within their power output capabilities, taking into account the temperature derating and input voltage derating. The maximum permissible output power that may be drawn from LPM409-CHAS and modules assembled in this chassis is given in *Figure1*.



For the power derating specification please see section 1.4 below.



# 2. Part Numbering and Ordering Information

### 2.1. Chassis Identification Numbering

First left-to-right sequence of the part number (4 characters):

• LPM409 Low Profile Modular 4-slot, 900W, with single-phase AC input. (Figure 2)

NOTE: Chassis and modules are RoHS-6 compliant.

- Dxxx LPM 4 09 Chassis type LMM - medical version Assembly facility code letter LPM - standard version & customer build ID No. Slot Slot Slot Slot 3 No. of slots 2 4 2-digit max. watts verstion 1-digit module selection code per (e.g. 09=900W) slot (0 - represents blank plate) Obtained from the LPM configurator on the company web site after entering your requirements.

Figure 2 - LPM 4-Slot Model Part Number Description



**Example:** After entering your requirements, the web Configurator could recommend the following optimized part number: LPM409-0HEG-DXXX which represents: Low Profile Modular Series with single-phase AC input, 4-slot, 900W chassis with modules of: blank panel "0" in slot 1 and modules H, E, G in slots 2 to 4, respectively. Besides the blanking panel in slots 1, the example's modules in this case would represent desired DC output voltages as follows: Slot 2 = H for 36V, Slot 3 = E for 3.3V and Slot 4 = G for 24V.

Assembly facility code and customer build ID No. are established during actual power supply assembly.

The slots numbering when viewing the unit from the rear, or output side is 1 - 6 from left to right.

### 3. Safety Specifications

### 3.1. Safety Approvals

#### LPM409 (Industrial)

- LMM409 (Medical)
- UL/CSA 60950-1 2nd
  - IEC 60950-1 2nd
- EN 60950-1 2nd
- CE Mark for LVD
- UL/CSA 60601-1, 3rd
  - IEC 60601-1, 3rd
  - EN 60601-1, 3rd









### 3.2. Insulation Safety Ratings

| TEST POINTS        | MINIMUM TEST VOLTAGE | INSULATION SAFETY RATING |
|--------------------|----------------------|--------------------------|
| Input-to-Chassis   | 2120 Vdc / min. 1s   | Basic Insulation         |
| Input-to-Output    | 4240 Vdc / min. 1s   | Reinforced Insulation    |
| Outputs-to-Chassis | 200 Vdc / min. 1s    | Functional Insulation    |
| Output-to-Output   | 200 Vdc / min. 1s    | Functional Insulation    |

### 4. Environmental and Reliability Specifications

### 4.1. Environmental Specification

| CONDITIONS / DESCRIPTION  | MIN  | NOM   | MAX   | UNITS   |
|---|--|---|---|---|
| nternal DC fan, air intake is from the AC connector side exiting at the DC module side        |  |   |   |   |
| Single unit, 4 modules assembled and full loaded, on a table at 1m distance.                  |  |   | 65  | dBA   |
| 50°C at Full power and up to 70°C at 50% load (linearly derated, for details see section 1.4) | -20  | 25  | 50  | °C  |
|   | -40  |   | 75  | °C  |
| 95% relative humidity @ 25 °C, non-condensing   |  |   |   |   |
| Operating: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles per axis                           |  |   | 1   | Gpk   |
| Non-operating: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles per axis                       |  |   | 4   | Gpk   |
| Operating: 11ms, half sine, 10 shocks per face  |  |   | 20  | Gpk<br>Gpk  |
|   | exiting at the DC module side Single unit, 4 modules assembled and full loaded, on a table at 1m distance.  10°C at Full power and up to 70°C at 50% load (linearly derated, for details see section 1.4)  15% relative humidity @ 25°C, non-condensing Departing: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles axis  150-operating: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles per axis | exiting at the DC module side Single unit, 4 modules assembled and full loaded, on a table at 1m distance.  10°C at Full power and up to 70°C at 50% load (linearly lerated, for details see section 1.4)  -40  15% relative humidity @ 25 °C, non-condensing Deparating: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles ler axis  Non-operating: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles per axis Deparating: 11ms, half sine, 10 shocks per face | exiting at the DC module side Single unit, 4 modules assembled and full loaded, on a table at 1m distance.  10°C at Full power and up to 70°C at 50% load (linearly lerated, for details see section 1.4)  -20  25  -40  25  25  26  27  28  29  29  29  29  20  20  20  20  20  20 | exiting at the DC module side Single unit, 4 modules assembled and full loaded, on a table tit 1m distance.  10°C at Full power and up to 70°C at 50% load (linearly lerated, for details see section 1.4)  25 50 40 75 105% relative humidity @ 25 °C, non-condensing Deparating: Swept 5-500-5Hz profile, 3 axis, 5 sweep cycles leraxis Non-operating: Swept 5-500-5Hz profile, 3 axis, 5 sweep leycles per axis Deparating: Swept 5-500-5Hz profile, 3 axis, 5 sweep leycles per axis Deparating: 11ms, half sine, 10 shocks per face |

### 4.2. Reliability

| PARAMETER         | CONDITIONS / DESCRIPTION  | MIN     | NOM | MAX | UNITS |
|-------------------|---|---------|-----|-----|-------|
| Calculated MTBF   | According to MIL-HDBK217, Ground benign 30°C, NOTE: Calculation was done for LPM409-EFGH. | 250,000 |     |     | hours |
| Demonstrated MTBF | Tamb = 25°C   | 250,000 |     |     | hours |





### 5. Fault Protection

| PARAMETER                        | CONDITIONS / DESCRIPTION  | MIN              | NOM         | MAX                 | UNITS       |
|----------------------------------|---|------------------|-------------|---------------------|-------------|
| Input Fuse                       | One fuse, non-user serviceable, located on line leg of AC input, Fast Acting type |                  | 12.5        |                     | Α           |
| Inrush Current Limitation        | Provided by NTC   | See              | Input Speci | <i>ification</i> se | ction       |
| Short Circuit Protection         | Provided by Current Limit circuit   | 105 <sup>1</sup> |             | 125 <sup>1</sup>    | %<br>lo_nom |
| Output Overvoltage<br>Protection | Latching (unit needs to be turned-off and on)                                     | 10               |             | 25                  | %<br>Vo_nom |
| Over Temperature                 | OT with auto restart  |                  |             | 70                  | °C amb      |

<sup>&</sup>lt;sup>1</sup> 100-150% for K, M modules 100-200% for L, N modules

# 6. EMC Specifications

### 6.1. EMC Immunity

| PARAMETER                             | CONDITIONS / DESCRIPTION   | CRITERION            |
|---------------------------------------|--|----------------------|
| Electrostatic Discharge (ESD)         | IEC/EN61000-4-2; GR-1089 R2-1, R2-2, R2-3<br>Level 4: contact: ±8 kV, air: ± 15kV  | Perform, criterion B |
| RF Susceptibility                     | IEC/EN 61000-4-3;<br>Level 3: 10 V/m; 80 MHz to 1000 MHz;<br>AM 80%, 1 kHz radiated; RF electromagnetic field                      | Perform, criterion A |
| Fast transient / burst                | IEC/EN 61000-4-4; Level 3; ±2 kV, 5kHz electrical fast transient / burst immunity test   | Perform, criterion B |
| Surge                                 | IEC61000-4-5, level 3; line to earth: ± 2kV, line to line: ± 1kV surge immunity test   | Perform, criterion B |
| RF Conducted Disturbance              | IEC/EN 61000-4-6; Level 3; GR-108;<br>10V, 0.15 to 80MHz, AM 80%, 1kHz   | Perform, criterion A |
|                                       | IEC/EN 61000-4-11; Voltage dips, interruptions and variations. (Interpretation: dip below Vi min with Po nom = hold-up time 10 ms) |                      |
|                                       | 1a: Dip 30%, 100ms   | Perform, criterion B |
| Valtana Dina / Chart                  | 1a: Dip 30%, 200ms   | Perform, criterion B |
| Voltage Dips / Short<br>Interruptions | 1a: Dip 60%, 10ms  | Perform, criterion A |
|                                       | 1a: Dip 60%, 100ms   | Perform, criterion B |
|                                       | 1a: Dip >95%, 10ms (interruption)  | Perform, criterion A |
|                                       | 1a: Dip >95%, 100ms (interruption)   | Perform, criterion B |

### 6.2. EMC Emission

| PARAMETER                          | CONDITIONS / DESCRIPTION   | CRITERION                                   |
|------------------------------------|--|---|
| Conducted Emission<br>EN55022, FCC | EN 55022, FCC – EN 55022, and CISPR 22 Class B, FCC 47CFR15 unintentional radiators; standalone at all nominal input voltages and measured in Po1: 0, 50%, 100%; signal connections open | Class B;<br>min. 3dB/μV margin              |
| Radiated Emission<br>EN55022, FCC  | EN 55022, FCC – EN 55022, and CISPR 22 Class B, FCC 47CFR15 unintentional radiators; standalone at all nominal input voltages and measured in Po1: 0, 50%, 100%; signal connections open | Radiated Class B QSP / AVG min. 3 dB margin |
| Input Current Harmonics            | EN 61000-3-2, sine wave input, Class D; measured standalone at all Vin nominal at power levels between 0 and rated   | Pass  |



# 7. Electrical Specifications

### 7.1. Input Specification

| PARAMETER               | CONDITIONS / DESCRIPTION   | MIN  | NOM     | MAX       | UNITS |
|-------------------------|--|------|---------|-----------|-------|
| Input AC Voltage Range  | Continuous AC voltage Range<br>Po=700W from 85-150 VAC<br>Po=900W from 150-264 VAC                                       | 85   | 115/230 | 264       | VAC   |
| Input Overvoltage Range | At max. power, no input OVP shutdown   |      |         | 300       | VAC   |
| Ground continuity       |  |      |         | 0.1 1     | Ohm   |
| Leakage Current         | @ 264 VAC, 60Hz, Commercial LPM / Medical LMM  |      |         | 1.5 / 0.5 | mA    |
| Frequency               | AC line  | 47   | 50/60   | 63        | Hz    |
| Power Factor            | Active PFC meets requirements of EN 61000-3-2 at full load, 120/230 VAC input  | 0.98 |         |           |       |
| Input Current           | Steady state, 85 VAC at 700W   |      |         | 11        | Arms  |
| Inrush Current          | 180 VAC, Max Power, 25°C,<br>acc. prETS300-132-1<br>230 VAC, Max Power, 25°C,<br>acc. prETS300-132-1 (Repeat rate >1min) |      | 40      |           | A pk  |
|                         | Vi = 230 VAC 100% loading  | 86   | 87      |           | %     |
| Efficiency 2            | Vi = 230 VAC 30-80% loading  | 80   | 82      |           | %     |
| Efficiency <sup>2</sup> | Vi = 115 VAC 100% loading  | 83   | 84      |           | %     |
|                         | Vi = 115 VAC 30-80% loading  | 78   | 80      |           | %     |

<sup>&</sup>lt;sup>1</sup> For any combination of output modules, any valid load and voltage setting

### 7.2. Output Specification

| PARAMETER                       | CONDITIONS/DESCRIPTION   | MIN   | NOM        | MAX                          | UNITS    |
|---------------------------------|--|-------|------------|------------------------------|----------|
| Output Power                    | One fan for internal cooling   |       | 900        |                              | W        |
| Output DC<br>Voltages / Modules | All output modules work in any chassis position and are max 1U high PCB-based  |       |            | -                            |          |
| Current Share <sup>3</sup>      | Active single-wire current share (modules E-J) Max. difference in currents between two modules– percentage of one module nominal current |       |            | ±10                          | %        |
| Line Regulation                 | Input from 85 to 264 VAC , 80% load  |       | 1.0        |                              | % Vo_nom |
| Load Regulation                 | From 0-100% load, Input >180VAC, Vo_nom  |       | 1.0        |                              | % Vo_nom |
| Thermal Drift                   | After 15 minute warm-up period   |       | 0.02       |                              | %/°C     |
| Total Regulation                | Variation of line, load and temperature drift  |       | 2.0        |                              | % Vo_nom |
| Output Adjust. Range            |  | 3     | See Modu   | le Selection                 | Table 1  |
| Dynamic Response                | Deviation for 10-90% or 90-10% load changes at a rate of 1A/µs, (constant current mode, Vo reach 1% band around Voset)                   |       |            | 4% <sup>1,2</sup><br>2000μs  | -        |
| Dynamic nesponse                | Deviation for 50-100% or 100-50% load steps with 1A/µs rate. (constant current mode, Vo reach 1% band around Voset)                      |       |            | 3% <sup>1</sup><br>400µs     | -        |
| Output Ripple & Noise           | BW = 20MHz; Filter 10nF/10uF; over line and load, 25°C   |       |            | 1% of<br>Vo_nom <sup>1</sup> | mVpk-pk  |
| CM Noise                        | Output to chassis, over line and load (Measured across 50 Ohms, with 10µH / 10nF in parallel)  |       | 220        | 500                          | mVpp     |
| Overshoot                       | Output voltage overshoot at turn-on  |       | 4          |                              | % Vo_nom |
| Turn-On Characteristics         | Turn ON at minimum and nominal output current  | Monot | tonic char | acteristic                   | -        |
| Turn-Off Characteristics        | Turn OFF at minimum and nominal output current   | Monot | tonic char | acteristic                   | -        |



<sup>&</sup>lt;sup>2</sup> Efficiency typical for standard configuration EFGH and nominal output voltage settings

| Turn-On Time            | Time required for output within regulation after initial application of AC input   |    |     | 1.5                  | s  |
|-------------------------|--|----|-----|----------------------|----|
| rum-on nine             | Time required for output within regulation after removing inhibit  |    |     | 100                  | ms |
| Hold-up Time            | Vo is required to stay within 95% regulation after AC is removed. Measured from the last AC peak, VAC min and full load. | 10 |     |                      | ms |
| Remote Sense 3          | Total compensation for cable losses  |    | 250 | 500                  | mV |
| Over-current Protection | Automatic recovery (Hiccup by K-N modules)   |    |     | 130 (180<br>for K-N) | %  |

- $^1$   $\,$  the E module dynamic deviation is less than 6%, the E, K, M module ripple is less than 2%  $\,$  the K and M module dynamic deviation is less than 7% and response time is below 1000us  $\,$  except K-N modules

### 7.3. Input - Signals, Features and Indicators Description

| SIGNAL NAME                               | PIN                   | DESCRIPTION  |
|---|-----------------------|--|
| Auxiliary Output                          | 1,2 vs 3,4<br>(RTN_D) | Output is available when the AC input is over 85VAC. The output nominal output voltage is 5.0V at maximum current rating of 1.0A. Over current protection occurs above 1.5A. The output is referenced to logic return, RTN_D.  |
| Input AC Indication - PFAIL -             | 5                     | Open Collector output with 20mA pull-down capability referenced to logic return RTN_D. PFAIL open or High state indicates the warning that the input is out of limits (the input voltage falls below 85Vac or rises over 264Vac) 5 ms before the output goes below the lower regulation limit. PFAIL will turn-off the green Input OK LED. PFAIL LOW state indicates that the input voltage is within the operating range. |
| Fan Fail / OTP Indication<br>- FAN_FAIL - | 6                     | Open Collector output pin with 20mA pull-down capability referenced to logic return RTN_D. FAN_FAIL OPEN OR HIGH state indicates a fan failure/over temperature protection (OTP) condition min. 100 ms before the unit shuts-down. A fan failure/OTP condition will turn-off the green FAN OK LED. FAN_FAIL LOW state indicates normal fan operation and no OTP condition.   |

### 7.4. Output - Signals, Features and Indicators Description

### 7.4.1.Modules E, F, G, H, J

| PIN | DESCRIPTION  |
|-----|--|
| 1   | Output voltage sense wire. Internally connected to Vout+ via 51 $\Omega$ . It is recommended to connect sense wire at positive load point.   |
| 2   | Output voltage sense wire. Internally connected to Vout- via 51 Ω. It is recommended to connect sense wire at negative load point.   |
| 3   | Common wire for parallel connected modules to achieve proper current sharing between the modules. Referenced to the Common pin. Interconnect CS pin on all parallel working modules. Active current share pin enables control of output voltage. Pulling-up this pin to 5V is possible to increase the output voltage. Pull-down of this pin has no effect. Voltage on this pin is 3.5V at nominal module current. |
| 4   | Reference pin to CSH   |
| 5   |  |
| 6   |  |
| 7   | Open collector output with 20mA pull-down capability, max. 15V (recommended Rpull-up = 1k, Vcc = 5V). Referenced to PG_LO. PG_HI OPEN OR HIGH state indicates that the module output voltage is below lower regulation limit. A PG_HI fail state turns the GREEN DC OK LED to RED. PG_HI LOW state indicates the module output voltage is within normal operation limits.  |
| 8   | Reference pin to PG_HI   |
| 9   | An opto-isolated input with 2-10mA current capability. Apply +(3-15)V on INH_HI and RTN on INH_LO (e.g. from the Auxiliary Output 5V) to activate the inhibit function. INH_HI OPEN OR LOW state ENABLES the module output. INH_HI HIGH state INHIBITES the module output.   |
| 10  | Reference pin to INH_HI  |
|     | 1 2 3 4 5 6 7 8 9  |



### 7.4.2. Modules K, L, M, N

| CIONIAL NIAME                        | DIN | PERCENTION   |
|--------------------------------------|-----|--|
| SIGNAL NAME                          | PIN | DESCRIPTION  |
| - INH_LO_B -                         | 1   | Reference pin to INH_HI_B  |
| Output Inhibit Function - INH_HI_B - | 2   | An opto-isolated input with 2-10mA current capability. Apply +(3-15)V on INH_HI_B and RTN on INH_LO_B (e.g. from the Auxiliary Output 5V) to activate the inhibit function. INH_HI OPEN OR LOW state ENABLES the module output. INH_HI HIGH state INHIBIT the module output.   |
| - PG_LO_B -                          | 3   | Reference pin to PG_HI_B   |
| Output Good Indication - PG_HI_B -   | 4   | Open collector output with 20mA pull-down capability, max. 15V (recommended Rpull-up=1k, Vcc=5V). Referenced to PG_LO_B. PG_HI open or High state indicates that the module output voltage is below lower regulation limit. A PG_HI fail state turns the GREEN DC OK LED to RED. PG_HI LOW state indicates that the module output voltage is within normal operation limits. |
| N/C                                  | 5   |  |
| N/C                                  | 6   |  |
| - INH_LO_A -                         | 7   | Reference pin to INH_HI_A  |
| Output Inhibit Function - INH_HI_A - | 8   | An opto-isolated input with 2-10mA current capability. Apply +(3-15)V on INH_HI_A and RTN on INH_LO_A (e.g. from the Auxiliary Output 5V) to activate the inhibit function. INH_HI OPEN OR LOW state ENABLES the module output. INH_HI HIGH state INHIBIT the module output.   |
| - PG_LO_A -                          | 9   | Reference pin to PG_HI_A   |
| Output Good Indication - PG_HI_A -   | 10  | Open collector output with 20mA pull-down capability, max. 15V (recommended Rpull-up=1k, Vcc=5V). Referenced to PG_LO_A. PG_HI open or High state indicates that the module output voltage is below lower regulation limit. A PG_HI fail state turns the GREEN DC OK LED to RED. PG_HI LOW state indicates that the module output voltage is within normal operation limits. |

### 7.5. Visual Alarms Description

|               | The second secon |  |
|---------------|--|--|
| ALARM NAME    | LED POSITION   | DESCRIPTION  |
| Input AC Good | Front panel – LED2<br>(see <i>Fig.7</i> )  | ON state LED indicates operation within specified input voltage range. GREEN LED indicator goes to an OFF condition on PFAIL signal failure state.   |
| Fan Good      | Front panel – LED1<br>(see <i>Fig.</i> ∕)  | ON state LED indicates normal fan operation and no OTP status.  GREEN LED indicator goes to an OFF condition on FAN_FAIL / OTP signal failure state. |
| Output Good   | Module connector / rear side (see <i>Fig.5</i> and <i>Fig.6</i> )  | GREEN LED indicates that module output voltage is over minimum regulation limit. GREEN LED indicator goes to RED on PG_HI signal failure state.      |

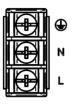
### 8. Connector Details

### 8.1. Input Connector Information

LPM409 front panel connectors pinout refers to Fig.4 and chapter 8.2 and 8.3. The signal connector is Cvilux part number: Cl3306P1H10, the mating part is Cvilux Cl3306S0010, Molex 50-57-9406 or Taiwan King Pin P553L-06(LF). **NOTE:** See chapter 10.1 Mating Connections and cables for Input cable information.

Figure 3 – LPM409 Front Panel Connectors Pinout View







### 8.2. Input Power Connector - Pinout

| SIGNAL NAME | PIN#     | ТҮРЕ                 | RECOMMENDED WIRES                    | V MAX<br>I MAX |
|-------------|----------|----------------------|--------------------------------------|----------------|
| Earth       | <b>(</b> | Earth / Chassis      | Min. 0.75 mm <sup>2</sup>            |                |
| AC Neutral  | N        | Input Power AC       | e.g. Interpower 86230120             | 264 Vrms       |
| AC Line     | L        | Input Power AC Fused | Max. torque on screws (M4x6): 1,5 Nm | 10 Arms        |

### **CAUTION:**

The plastic cover must be in place when AC input voltage is applied to the unit! The cover may be removed (with the help of a screwdriver) only when the input cable is disconnected from the mains.

# 8.3. Input Signal Connector – Pinout (Mating connector: Molex 50-57-9406, Pins: Molex 16-02-0082)

| SIGNAL<br>NAME | PIN# | WIRE COLOR | TYPE                            | SIGNAL<br>REFERENCE | LOW LEVEL<br>HIGH LEVEL | V MAX<br>I MAX |
|----------------|------|------------|---------------------------------|---------------------|-------------------------|----------------|
| +5V            | 1    | Red        | Aux Output                      | RTN_D               | -                       | 5.0Vdc         |
| +5V            | 2    | Red        | Aux Output                      | RTN_D               | -                       | 1.0Adc         |
| RTN_D          | 3    | Black      | Logic Reference Potential       | -                   | -                       | -              |
| RTN_D          | 4    | Black      | Logic Reference Potential       | -                   | -                       | -              |
| PFAIL          | 5    | Yellow     | Open Collector Output + ZD 5.6V | RTN_D               | <400mV@ 20mA<br>Pull up | -<br>20mA      |
| FAN_FAIL       | 6    | Green      | Open Collector Output + ZD 5.6V | RTN_D               | <400mV@ 20mA<br>Pull up | -<br>20mA      |

### 8.4. Module Power Outputs Pinout

### 8.4.1.Modules E, F, G, H, J - A, B Bus-Bar

| SIGNAL NAME | PIN#  | TYPE            | SIGNAL<br>REFERENCE | LOW LEVEL<br>HIGH LEVEL | V MAX<br>I MAX                         |
|-------------|-------|-----------------|---------------------|-------------------------|--|
| Vout+       | Vout+ | Output Power DC | Vout-               | -                       | See <i>Module</i><br>Selection Table 1 |
| Vout-       | Vout- | Output Power DC | -                   | -                       | See <i>Module</i><br>Selection Table 1 |

**Connector type:** Bus-bar see *Figure 4* 

Mating part: Ring terminal for M4 screw, with appropriate cross section for wire.

### 8.4.2. Modules K, L, M, N

| 0.T.Z.1VIO  | uuics ix, E, | 171, 17           |                     |                         |  |
|-------------|--------------|-------------------|---------------------|-------------------------|--|
| SIGNAL NAME | PIN#         | TYPE              | SIGNAL<br>REFERENCE | LOW LEVEL<br>HIGH LEVEL | V MAX<br>I MAX                         |
| Vout1+      | Vout1+       | Output 1 Power DC | Vout1-              | -                       | See Module<br>Selection Table 1        |
| Vout1-      | Vout1-       | Output 1 Power DC | -                   | -                       | See <i>Module</i><br>Selection Table 1 |
| Vou2+ 1     | Vout2+       | Output 2 Power DC | Vout2-              | -                       | See <i>Module</i><br>Selection Table 1 |
| Vout2- 1    | Vout2-       | Output 2 Power DC | -                   | -                       | See <i>Module</i><br>Selection Table 1 |

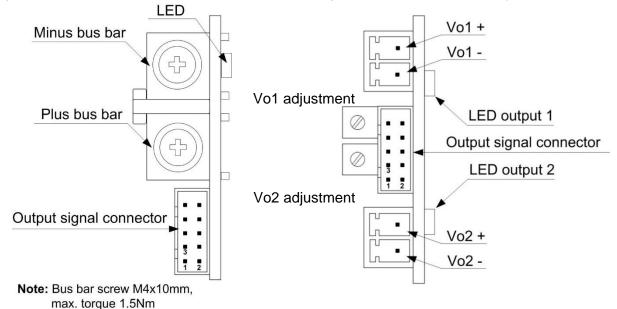
 $<sup>^{\</sup>mbox{\scriptsize 1}}$  valid only for two outputs modules M and N.

**Connector type:** Phoenix Contact 1803277 **Mating part:** Phoenix Contact 1850660



Figure 4 - Modules E,F,G,H,J Power Output Connectors

Figure 5 - Modules K,L,M,N Power Output Connectors



### 8.5. Module Signal Output Connector Pinout

The output signal connector provides signal information across its 10-pin output, for position of the output signal connector please see *Figure 4* and *Figure 5*.

Connector type: JST S10B-PHDSS-B

**Mating part:** JST housing PHDR-10VS, pins SPHD-002T-P0.5 (AWG 28-24) or SPHD-001T-P0.5 (AWG 26-22)

#### Modules E, F, G, H, J

|             | ,, - |  |                  |                                    |                |
|-------------|------|--|------------------|------------------------------------|----------------|
| SIGNAL NAME | PIN# | TYPE   | SIGNAL REFERENCE | LOW LEVEL<br>HIGH LEVEL            | V MAX<br>I MAX |
| VS+         | 1    | Output voltage sense wire. Internally connected to Vout+ via 51 $\Omega$ | Vout+            | -                                  | 0.5V<br>10mA   |
| VS-         | 2    | Output voltage sense wire. Internally connected to Vout- via 51 $\Omega$ | Vout-            | -                                  | 0.5V<br>10mA   |
| CSH         | 3    | Active Current Share pin   | Common           | -                                  | 5V<br>-        |
| Common      | 4    | Reference pin to CSH   | -                | -                                  | =.             |
| N/C         | 5    | -  | -                | -                                  | -              |
| N/C         | 6    | -  | -                | -                                  | =.             |
| PG_HI       | 7    | Open collector output  | PG_LO            | <400mV@5mA<br>Pull up              | 15V<br>20mA    |
| PG_LO       | 8    | Reference for PG_HI  | -                | -                                  | -<br>20mA      |
| INH_HI      | 9    | Input, protected by 18V Zener diode                                      | INH_LO           | LED with a serial resistor 2 kOhms | 15V<br>10mA    |
| INH_LO      | 10   | Reference for INH_HI   | -                | -                                  | -<br>10mA      |



8.5.1. Modules K, L, M, N

| SIGNAL NAME | PIN# | TYPE                                | SIGNAL<br>REFERENCE | LOW LEVEL<br>HIGH LEVEL            | V MAX<br>I MAX |
|-------------|------|-------------------------------------|---------------------|------------------------------------|----------------|
| INH_LO_B    | 1    | Reference for INH_HI_B              | -                   | -                                  | -<br>10mA      |
| INH_HI_B    | 2    | Input, protected by 18V Zener diode | INH_LO_B            | LED with a serial resistor 2 kOhms | 15V<br>10mA    |
| PG_LO_B     | 3    | Reference for PG_HI_B               | -                   | -                                  | -<br>20mA      |
| PG_HI_B     | 4    | Open collector output               | PG_LO_B             | <400mV@5mA<br>Pull up              | 15V<br>20mA    |
| N/C         | 5    | -                                   | -                   | -                                  | -              |
| N/C         | 6    | -                                   | -                   | -                                  | -              |
| INH_LO_A    | 7    | Reference for INH_HI_A              | -                   | -                                  | -<br>10mA      |
| INH_HI_A    | 8    | Input, protected by 18V Zener diode | INH_LO_A            | LED with a serial resistor 2 kOhms | 15V<br>10mA    |
| PG_LO_A     | 9    | Reference for PG_HI_A               | -                   | -                                  | -<br>20mA      |
| PG_HI_A     | 10   | Open collector output               | PG_LO_A             | <400mV@5mA<br>Pull up              | 15V<br>20mA    |

# 9. Mechanical Drawings

**Overall Dimensions:** 11.9" x 3.6" x 1.6" (303.7 x 92 x 40.64mm)

Weight: 1.38 kg (including 4 modules)

All drawing dimensions are shown in millimeters, unless otherwise noted.

Figure 6 - Front View

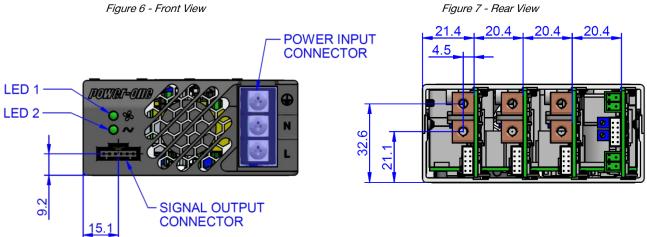


Figure 8 - Side View

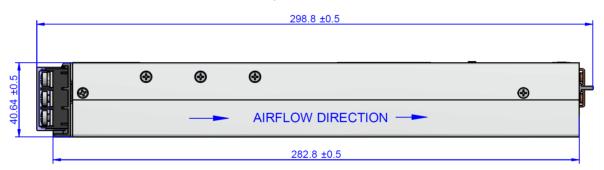




Figure 9 - Top View

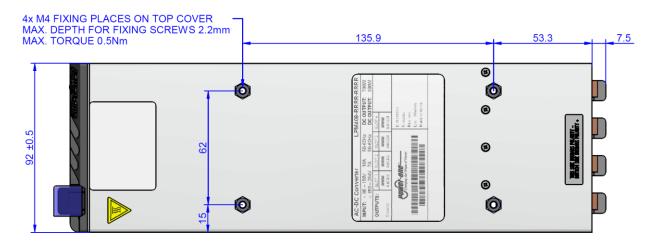
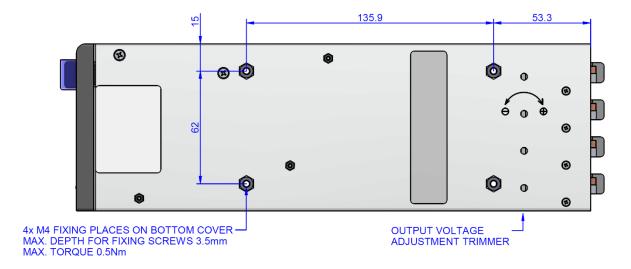


Figure 10 - Bottom View



### 10. Accessories

### 10.1. Mating Connections and Cables

All the power and signal cables and mating connectors are not included in the LPM616 standard package. These all need to be ordered extra.

| Front panel signal cable:            | Bel Power Solutions accessory LPM000-LEAD-03  | see <i>Figure 11</i>  |
|--------------------------------------|---|-----------------------|
| Output signal cable:                 | Bel Power Solutions accessory LPM000-LEAD-04  | see Figure 12a        |
| Mating connector:                    | JST, housing PHDR-10VS, pins SPHD-002T-P0.5 (AWG 28-24)                                     |                       |
| Output power cable (for LPM126):     | 14AWG – 10AWG depend on the output current, min. thermal class 105°C, lug terminal 4mm 6mm² |                       |
| Output power cable (for LPM109/118): | Bel Power Solutions accessory LPM000-LEAD-05,   | see <i>Figure 12b</i> |





Figure 11 - LPM000-LEAD-03: Front panel signal cable (length of the cable 1.8m)



Figure 12a - LPM000-LEAD-04: Output signal cable (length of the cable 0.75m)



Figure 12b - LPM000-LEAD-05: Output power cable for modules K-N (length of the cable 0.75m)

### 10.2. Signal Output Wire Colors

|      | SIGNAL NAME BY        | SIGNAL NAME BY     |            |
|------|-----------------------|--------------------|------------|
| PIN# | MODULES E, F, G, H, J | MODULES K, L, M, N | WIRE COLOR |
| 1    | VS+                   | PG_HI_A            | Red        |
| 2    | VS-                   | PG_LO_A            | Blue       |
| 3    | CSH                   | INH_HI_A           | Yellow     |
| 4    | Common                | INH_LO_A           | White      |
| 5    | N/C                   | N/C                | -          |
| 6    | N/C                   | N/C                | -          |
| 7    | PG_HI                 | PG_HI_B            | Violet     |
| 8    | PG_LO                 | PG_LO_B            | Brown      |
| 9    | INH_HI                | INH_HI_B           | Black      |
| 10   | INH_LO                | INH_LO_B           | Green      |

### 10.3. Paralleling Busbars (only for modules E-J)

| ITEM           | DESCRIPTION   | MODEL |
|----------------|---|-------|
| LPM000-BBAR-07 | Busbar for parallel connection of two modules, central screw M5, max. 100A <sup>1</sup> . Two pieces are in one package LPM000-BBAR-07. |       |
| LPM000-BBAR-08 | Busbar washer with 1 opening for odd module paralleling. Two pieces are in one package LPM000-BBAR-08.                                  | 0     |

#### NOTES:

<sup>1</sup> For load current over 100A use two or more cables connected to separate busbars.

<sup>&</sup>lt;sup>3</sup> Modules K-N cannot be connected in parallel.

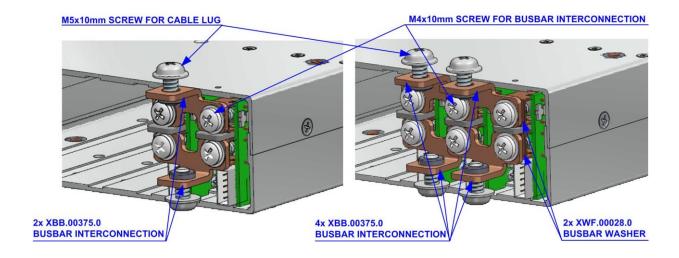


<sup>&</sup>lt;sup>2</sup> By using busbars (paralleling modules) you will need also the signal output leads LPM000-LEAD-04, one for each connected module. The interconnection is shown in *Figure 15* (especially all pins 3 - CSH and all pins 4 - Common must be reconnected).

The example of LPM409-00EE-DXXX assembly configuration (two modules in parallel) is shown in *Figure 13*. One set of busbars LPM000-BBAR-07 + 2x LPM-LEAD-04 is necessary for connection.

Figure 13 – LPM409-0000EE-DXXX Parallel Configuration

Figure 14 – LPM409-000EEE-DXXX Parallel Configuration



An example of an LPM409-0EEE-DXXX assembly configuration (three modules in parallel) is in *Figure14*. Two sets of busbars LPM000-BBAR-07 + one set of LPM000-BBAR-08 + 3x LPM-LEAD-04 are necessary to make the interconnections.

Note, for odd number of modules in parallel it is necessary to also use one set of busbar washers LPM000-BBAR-08.

To connect the load for higher output currents use more cables for each pole. For example for 200A output current (LPM409-EEEE-DXXX) connect minimal two power cables for each pole (2x 42mm² for one pole). Use min. 105°C thermal class isolation.

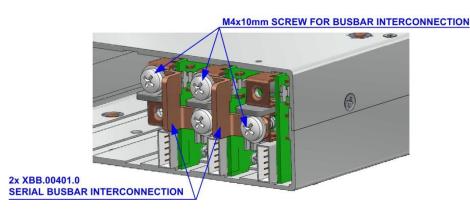


Primary Secondary LPM409-CHAS LPM126-OUTA1 module PFC Stage, Inverter Vout -7,8 PG AC Line (fused) Vout Phase Mains 9,10 INH D OPEN/LOW - Enable VS+ 3 CSH Ø AC Netral HIGH - Disable 4 Common VS- 2 Ø Chassis Vext O-LPM126-OUTA1 module 7,8 PG Vout + Load Vout 9,10 INH **Strength Voltages:** Primary to Secondary Primary to Chassis 4240Vdc OPEN/LOW - Enable 3 CSH VS+ 2120Vdc HIGH - Disable 4 Common **VS-2** DC Line Droop Secondary to Chassis 200Vdc Compensation LPM409-CHAS Vext O-LPM126-OUTA1 module Auxiliary 5V Bias & Signals STATUS 7,8 PG Vout + 1,2 5V AUX Vout -9,10 INH **PFAIL** HIGH = FAIL OPEN/LOW - Enable 3 CSH VS+ FAN\_FAIL HIGH - Disable VS- 2 4 Common 3,4 RTN D **PIN DETAILS:** - External Voltage referenced to pin 8, max.15V Ø Power Supply Vext INH - LED + 2k res. Input, 18V Zener Diode Protection, max. 15V O Signal Supply PG - Open Collector, max. 20mA Signal Output **PFAIL** - Open Collector, 5.1V Zener Diode Protection, max. 20mA Signal Input FAN\_FAIL - Open Collector, 5.1V Zener Diode Protection, max. 20mA Logic Return PG and INH pins are galvanically isolated each other DO NOT INTERCONNECT Common & Vout -!

Figure 15 - LPM409 - Paralleling Connection Diagram for LPM126 Modules

### 10.4. Serial Busbars (only for modules E-J)

| ITEM           | DESCRIPTION                                  | MODEL |
|----------------|--|-------|
| LPM000-BBAR-09 | Busbar for serial connection of two modules. |       |





Primary Secondary LPM409-CHAS Vext O LPM126-OUTA1 module PFC Stage, Inverter 7.8 PG Vout + AC Line (fused) Vout -Phase Mains 9,10 INH OPEN/LOW - Enable 3 CSH VS+ Ø AC Netral HIGH - Disable **VS-2** 4 Common Ø Chassis LPM126-OUTA1 module Vout -7,8 PG Load Vout 9,10 INH Strength Voltages: Primary to Secondary OPEN/LOW - Enable VS+ 3 CSH HIGH - Disable Primary to Chassis 2120Vdc **VS-2** Common DC Line Droop Secondary to Chassis 200Vdc Compensation LPM409-CHAS LPM126-OUTA1 module Auxiliary 5V Bias & Signals STATUS 7,8 PG 5V AUX Vout -9.10 INH **PFAIL** OPEN/LOW - Enable 3 CSH VS+ FAN\_FAIL 6 HIGH - Disable VS- 2 Common 3,4 RTN\_D Reverse bias diodes may be required for certain applications, eg. large capacitive loads. Recommended diode valuse: Vrr = 100Vdc, I<sub>F</sub>= 0.2x module max. current **PIN DETAILS:** - External Voltage referenced to pin 8, max.15V Ø Power Supply Vext INH - LED + 2k res. Input, 18V Zener Diode Protection, max. 15V Signal Supply PG - Open Collector, max. 20mA Signal Output - Open Collector, 5.1V Zener Diode Protection, max. 20mA **PFAIL** Signal Input FAN\_FAIL - Open Collector, 5.1V Zener Diode Protection, max. 20mA Logic Return PG and INH pins are galvanically isolated each other DO NOT INTERCONNECT Common & Vout -!

Figure 17 - LPM409 - Serial Connection Diagram for LPM126 Modules

### For more information on these products consult: tech.support@psbel.com

**NUCLEAR AND MEDICAL APPLICATIONS** - Products are not designed or intended for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems.

**TECHNICAL REVISIONS** - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice

